Fuzzy-Decision-Making Predictive Power Control Approach to On-Grid Photovoltaic Panel

Ali Jafer Mahdi1, Majid Aryanezhad2*

1Electrical Engineering Department, University of Kerbala, Kerbala, Iraq, ali.j.mahdi@uokerbala.edu.iq
2Electrical Engineering Department, Shahid Chamran University, Ahvaz, Iran, m.aryanezhad@ieee.org

Abstract

In this paper, a voltage-current mode controller is proposed to adjust both the input current and DC voltage of a boost-inverter in order to improve the performance of the photovoltaic (PV). The proposed system is based on fuzzy-making-decision predictive voltage-current controller (FPVCC). The simulation results show that in case of using the proposed FPVCC, the input current of a PV array is in the limited value (i.e., is approximate 27% of the short-circuit current of a PV panel) compared with a classical voltage controller, an over-current is passing through PV array (i.e., is about 155% of the short-circuit current). Also, the waveform of the output voltage is sinusoidal with low total harmonic distortion (THD), i.e. 4.21%, and the power obtained from a PV array with FPVCC is approximately 6 times of power obtained without a controller.

Keywords: PV, Fuzzy making decision, Predictive power control.

Article history: Received 05-May-2017; Revised 15-May-2017; Accepted 03-Jun-2017.
© 2017 IAUCTB-IJSEE Science. All rights reserved

1. Introduction

A) Motivation

Photovoltaic (PV) panels can be categorized into two types: off-grid and on-grid. For off-grid solar panels, the main electricity is only fed from PV panels and battery bank. These systems are suitable for remote area which is typically isolated from local grid. To maximize the power withdrawn from PV panels, maximum power point tracking (MPPT) charge controller is used by converting the variable DC voltage into maximum power point voltage. The important device in off-grid solar systems is the grid inverter which takes the DC power from both PV panels and batteries then converts it into AC power to directly supply the building. The inverters are classified into two types: grid tie inverter and normal inverter. The former one converts the solar power directly to main electricity. While, the latter one converts the DC power from PV panels and battery bank to main electricity. Moreover, the latter one is used for charging the battery bank [1-2]. For grid-connected solar power systems, the building is fed from both local grid and a PV array in order to cover the consumer’s own power demand and decrease electricity bills [3]. The PV panels are connected with a grid tie inverter that directly converts DC power into AC power (220 V, 50 Hz). For grid-connected solar systems with a storage system, a MPPT charge controller, i.e. DC-DC converter, is used for charging the bank of batteries that connects with a separate inverter. The building in these systems can be supplied during power grid failure. The drawbacks of these systems are the big losses in power electronic converters and the high cost of maintenance. Basically, there are two types of grid tie inverter topologies: central inverters and micro inverters. Central inverter is the typical choice for high power solar systems. Its efficiency is better than the other type, but it misses MPPT operation for each PV panel due to the fluctuations in atmosphere, e.g. shading and clouding. For micro inverters, each PV panel has own small size inverter, which achieves optimal power conversion for each PV panel. For these inverters, if any PV panel is shaded or is not completely pointed to the sun, the total DC power is not highly affected [4].

B) Literature survey

Several studies have been conducted to design and analysis of grid-connected solar power systems with or without energy storage devices. Ref. [5] designed and constructed a mini-solar power plant
connected with local grid. Each part of the solar plant is modelled and analyzed in order to estimate the behavior of the grid under various operating conditions. In [6], power electronic circuits including a buck-boost converter with a full-brig inverter was presented to supply AC power with high power factor. In [6], the waveform of the output AC voltage was improved by selecting a high switching frequency for a buck-boost converter that operates in the discontinuous-current conduction-mode (DCCM) to avoid additional input inductor current controller. It is worth noting that most of grid-connected PV inverters have been assessed by using unreal solar irradiance profile such as steps and ramps or triangle ramps. Although this test evaluation is easy to perform, it may not be representing the dynamic response of the system under actual conditions [7].

C) Contribution

In this paper, a grid-connected solar PV power system is designed for supplying a stable AC power without batteries. The system includes a PV array connected to grid via a boost-inverter and an LC filter. Both the input current and the output DC voltage are adjusted by controlling the duty cycles of a boost converter. The purpose of controlling the input current and DC voltage is respectively, to prevent the over-current passing through a PV array and to supply a constant AC voltage. The proposed fuzzy-decision-making predictive voltage-current controller (FPVCC) is used to optimize of the output power by controlling the current and voltage simultaneously. Simulation results present a stable AC power to grid (220 V, 50 Hz) with a high power-factor and low THD.

D) Organization of the paper

This paper is coordinated as follows: In Section II, the proposed PV power system is presented with a real solar irradiance profile. Section III gives the modelling and design steps of the proposed system. Section IV presents the proposed FPVCC to control of voltage-current and Section V shows the simulation and performance analysis of the proposed PV power system. Conclusions are given in Section VI.

2. Diagram of the Proposed PV Power System

Figure 1 shows the diagram of the proposed solar PV power system including a PV array, which supplies a grid via a boost-inverter. Two control signals (i.e., PWM1 for adjusting both the input current and DC voltage, and PWM2 for controlling the output AC voltage) are essential to generate a reliable output power under real fluctuated irradiances. An LC filter is connected between an inverter and the local grid to achieve a sinusoidal AC voltage.

A) Modelling and Design of the Proposed PV

− Real Irradiance Profile:

In this paper, real solar irradiances were measured over a day in a case study in Ahvaz city as shown in Fig. 2. The curve presents how the solar irradiance changes during sunshine hours. At 1:00 pm, the sun is at the peak point, the solar irradiance is approximately 960 W/m². But, at 8:00 am and 6:00 pm, the solar irradiances are about 260 W/m² and 330 W/m², respectively. It is worth noting that the average solar irradiance is above 650 W/m², which is considered a sunny day [8].

− PV Array Simulator

PV panel is a device utilized to convert irradiance from the sun directly into DC power. Typically it made of multiple silicon solar cells joint between glass and a backing material [1]. An embedded MATLAB function is used to simulate a PV array. In literatures, a PV panel is simulated as a single diode exponential model that provides fairly accurate current-voltage characteristics, since this model does not include the effect of parallel resistance, the I-V characteristics [9]. In this paper, a PV panel is simulated as a logarithmic model using Eqs. (1)-(3) [10]. This PV model requires four input parameters, which are listed in Table 1.

![Diagram of the solar power system](image-url)
Figure 4 shows the proposed PV array simulator. The purpose of a boost-inverter is to convert DC power from PV array to a stable AC power with the following specifications: (i) constant voltage and frequency, i.e. 220 V and 50 Hz; (ii) high power controller and (iii) low total harmonics distortion (THD) for both voltage and current. In this paper, a boost converter, a voltage controller adjusts the duty cycle, D, to keep the load voltage constant [12] (e.g., 311 V) and load current, I_{load}, is set 2 A. In order to avoid switching losses, the switching frequency, f_{sw}, is set to 10 kHz for the initial design. If satisfy results are obtained with this frequency, the size and cost of a boost converter are decreased [13]. The variation in input inductor current, ΔI_{pv}, is chosen 50% less than of the average load current for all operating conditions. Furthermore, the perunit DC voltage ripple, (ΔV/V_{dc}), is set less than 5%.

According to above initial design considerations, the minimum values of the boost inductor, L_{min}, and filter capacitor, C_{min}, are calculated via the following steps [12].

1. Calculating D with minimum PV voltage.
   \[ D = 1 - \frac{V_{pv}}{V_{dc}} \]  

2. Calculating the average value of inductor current, Ipv.
   \[ I_{pv} = \frac{V_{dc} \cdot I_{load}}{V_{pv}} \]  

3. Determining the minimum inductance, L_{min}.
   \[ L_{min} = \frac{V_{pv} \cdot D}{M_{pv} \cdot f_{sw}} \]  

4. Repeating steps 1-3 for computing the maximum boost inductance, L_{max}, with maximum PV voltage.
5. Finally, calculating the minimum capacitance, C_{min}, with maximum PV voltage.
   \[ C_{min} = \frac{D}{\left( \frac{V_{dc} - \Delta V}{I_{load}} \right) f_{sw}} \]  

### Table 1.
Parameter values of a PV panel [11]

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Open circuit voltage, V_{oc}</td>
<td>28.4 V</td>
</tr>
<tr>
<td>Short circuit current, I_{sc}</td>
<td>7.92 A</td>
</tr>
<tr>
<td>Voltage at maximum power point, V_{m}</td>
<td>22.8 V</td>
</tr>
<tr>
<td>Current at maximum power point, I_{m}</td>
<td>7.11 A</td>
</tr>
<tr>
<td>Type of Array connection and number of PV panels, N_{pv}</td>
<td>Series, 5</td>
</tr>
</tbody>
</table>

\[
I_{pv} = -\lambda I_{sc}[1-C_1(\exp(V_{pv}/C_{oc}) - 1)]
\]  

\[
C_1 = (1 - I_{m}^{mpp}) \exp(-I_{m}^{mpp}/C_{2}^{mpp} V_{oc})
\]  

\[
C_2 = \ln(1 - I_{m}^{mpp}/I_{sc})
\]  

where \( \lambda \) is the per-unit solar irradiance at 1000 W/m².

Note that a PV panel can be also simulated as a dependent voltage-source, \( V_{pv} \), by rearranging Eq. (1). It is clear from Eq. (4) that the PV current, \( I_{pv} \), is firstly measured then entered to the proposed PV simulator.

\[
V_{pv} = C_2 V_{oc} \ln\left(\frac{1 - (I_{pv}/I_{sc})}{C_1}\right)
\]  

Figure 3 shows a PV panel Simulink model based on an embedded MATLAB function and a voltage-controlled source. It is worth noting that the upper limit of PV voltage does not exceed the open circuit voltage to protect the PV panel from overvoltage damage. Figure 4 shows the proposed PV array simulator, which continues five PV panels connected in series in order to increase the DC voltage and consequently reduce the size and cost of a boost converter.

The purpose of designing a boost-inverter is to convert DC power from PV array to a stable AC power with the following specifications: (i) constant voltage and frequency, i.e. 220 V and 50 Hz; (ii) high power controller and (iii) low total harmonics distortion (THD) for both voltage and current. In this paper, a boost converter is designed based on a variable input DC voltage, (e.g., 100 V to 130 V) and using real irradiance in Fig. 2. For a boost converter, a voltage controller adjusts the duty cycle, D, to keep the load voltage constant [12] (e.g., 311 V) and load current, I_{load}, is set 2 A. In order to avoid switching losses, the switching frequency, f_{sw}, is set to 10 kHz for the initial design. If satisfy results are obtained with this frequency, the size and cost of a boost converter are decreased [13]. The variation in input inductor current, ΔI_{pv}, is chosen 50% less than of the average load current for all operating conditions. Furthermore, the perunit DC voltage ripple, (ΔV/V_{dc}), is set less than 5%.
the inverter frequency (i.e., the PWM frequency of the carrier signal) is increased. Also, a sinusoidal filter (i.e., LC filter) is connected between an inverter and grid in order to obtain a smooth sine wave output voltage. As mentioned above, by increasing the inverter frequency, the cut-off frequency of an LC filter can be increased in order to decrease the size of filter inductance, L, and filter capacitance, C. Eq. (9) is used to design an LC filter at 500 Hz and the values of L and C will present in Table 2.

\[ f = \frac{1}{2\pi \sqrt{LC}} \]  

(9)

3. The Proposed Fuzzy-Making-Decision Predictive Voltage-Current Controller

For interfacing a PV power system with grid, a boost DC-DC converter and an inverter are essential with a DC voltage controller, which has several advantages such as: (i) reducing the complexity of the PWM controller for an inverter and (ii) maintaining a high dynamic response at the AC side. In conventional PV power systems, the DC voltage are controlled by only adjusting the duty cycles of a boost DC converter by FPVCC algorithm.

A)Predictive Voltage-Current Control Scheme

To implementation of the predictive voltage-current control (PVCC) algorithm can be divided in three main stages: current estimation, variable predictions and optimization of the cost function respect to current and voltage reference [14-15].

Predictions: The output current and voltage predictions are directly computed from the discrete model. However, the time needed to compute the DC voltage and takes a considerable portion of \( V_{dc} \), resulting in one sampling delay. Then, variables at \((k+1)\)\( V_{dc} \) are extrapolations used as an initial condition for variables at \((k+2)\)\( V_{dc} \). Then, we must to minimize two error of cost function \( g_1 \) and \( g_2 \).

\[ g_1(V_{s^{k+1}}) = (V_{dc}^{ref} - \hat{V}^{k+2}) \]  

(10)

\[ g_2(V_{s^{k+1}}) = (I^{ref}_pv - \hat{I}^{k+2}_pv) \]  

(11)

For optimization of the cost function \( g_1 \) and \( g_2 \) by using of (12) the two variables prediction \( (V_{dc}^{ref} \) and \( I_{pv}^{ref} \), will be pursued in one cost function \( G \).

\[ G = K_{g_1}g_1(V_{s^{k+1}}) + K_{g_2}g_2(V_{s^{k+1}}) \]  

(12)

Thus, the boost voltage vector that minimizes (13) is

\[ V_{s^{opt}} = \min \ G(V_{s^{k+1}}) \]  

(13)

where, \( V_{s^{opt}} \) is the optimal boost voltage inverter to apply in the next sampling time. The weighting factors of (12) are two: \( K_{g_1} \) and \( K_{g_2} \). For simplicity, the weighting factor associated with the dc voltage \( K_{g_1} \), is considered unity \( (K_{g_1} = 1) \) and \( K_{g_2} \) must be adjusted.

B)Fuzzy-Making-Decision Predictive Voltage-Current Controller

The proposed FPVCC strategy evaluates the two cost functions \( g_1 \) and \( g_2 \) in a separately fashion for each possible boost voltage converter vector saving the obtained error evaluation in two variable data structures. The operation of the FPVCC needs the specification of membership and decision functions.

\[ \nabla_1(V_{s^{k+1}}) = \frac{g_1^{max} - g_1(V_{s^{k+1}})}{g_1^{max} - g_1^{min}} \]  

(14)

where, \( \nabla_1 \) is the membership function for correspondent to \( i \)th criterion. Therefor we choose two membership functions \( \nabla_1 \) and \( \nabla_2 \) for PV-current and dc-voltage respectively.

\[ \nabla_1(V_{s^{k+1}}) = \frac{g_1^{max} - g_1(V_{s^{k+1}})}{g_1^{max} - g_1^{min}} \]  

(15)

\[ \nabla_2(V_{s^{k+1}}) = \frac{g_2^{max} - g_2(V_{s^{k+1}})}{g_2^{max} - g_2^{min}} \]  

(16)

\[ \nabla_1 \) and \( \nabla_2 \) membership functions values to the range \([0,1]\). The final selection of the best voltage actuation is performed by the decision function. Then, considering that N objectives are fairly optimized a maximizing decision, minimizing decision and the AND operator could be stated as following:

\[ \nabla_p(V_{s^{k+1}}) = \nabla_1(V_{s^{k+1}}) \nabla_2(V_{s^{k+1}}) \]  

(17)

4. Analysis of Simulation Results

In this section, the simulations are conducted in order to validate the effectiveness of the proposed controller under solar irradiances (see Fig. 2). A solar PV power system with the proposed controller is implemented in MATLAB/Simulink using power system blocks as shown in Fig. 5. Note that voltage and current sensors are applied to obtain (i) the input DC voltage and current; (ii) the AC voltage and
current and (iii) the active and reactive powers. The system parameters are given in Table 2.

A) Performance of DC Side

DC side includes both a PV array and a boost converter. As mentioned in Section 4, the maximum input current should be equal (or less) to the short-circuit current of a PV panel to prevent overcurrent damage. Figures (6) and (7) show the time response of transient and steady-state current of a PV array, respectively. The performance data (using a classical controller and the proposed controller), which are the peak transient PV current, $I_{pv\_peak}$ and PV ripple current, $\Delta I_{pv}$, are listed in Table 3.

![Figure 6: PV current transient waveform](image)

![Figure 7: PV current steady-state waveform](image)

![Figure 5: Simulink model of the proposed solar-PV power system](image)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Boost inductance, $L$, and resistance</td>
<td>0.6 mH, 1 mΩ</td>
</tr>
<tr>
<td>DC-link capacitance, $C$</td>
<td>100 µF</td>
</tr>
<tr>
<td>Filter inductance, $L_f$, and resistance</td>
<td>10 mH, 1 mΩ</td>
</tr>
<tr>
<td>Filter capacitance, $C_f$</td>
<td>10 µF</td>
</tr>
<tr>
<td>Load resistance, $R$</td>
<td>110 Ω</td>
</tr>
<tr>
<td>Boost switching frequency, $f_{sw}$</td>
<td>10 kHz</td>
</tr>
<tr>
<td>Inverter frequency, $f_{inv}$</td>
<td>15 kHz</td>
</tr>
<tr>
<td>Sampling period, $T_s$</td>
<td>1 µsec</td>
</tr>
</tbody>
</table>

![Table 2: Parameter values](image)

Note that in case of using the proposed FPVCC controller, the input peak current is about 27% of $I_{sc}$, while in case of using a classical controller the input current is approximately 155% of $I_{sc}$. Also, in case of using the proposed controller, the ripple current is reduced to 5.2 A, while in case of using a classical controller the ripple current is 10.49 A.

B) Performance of AC Side

AC side includes an inverter and an LC filter. The quality assessment of a solar PV power is mainly based on: (i) keeping the output AC voltage and frequency constant; (ii) minimizing the THDs of the output voltage and current. In this research, the inverter frequency is changed in the range of 100 Hz to 15 kHz to demonstrate the quality of output voltage and current. The performance results are summarized in Table 4. It is clear that the THDs of output voltage and current at 15 kHz meet the limits proposed in IEEE standard, i.e. THD should be no higher than 5%. Figures (8) and (9) respectively illustrate the output voltage and current waveforms with their FFT analysis at 15 kHz inverter frequency.

![Table 3: Performance of DC Side](image)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Classical Controller</th>
<th>Proposed Controller</th>
</tr>
</thead>
<tbody>
<tr>
<td>$I_{pv_peak}$</td>
<td>12.27 A &gt; $I_{sc}$</td>
<td>2.19 A &lt; $I_{sc}$</td>
</tr>
<tr>
<td>$\Delta I_{pv}$</td>
<td>10.49 A</td>
<td>5.20 A</td>
</tr>
</tbody>
</table>

![Table 4: Effect of inverter frequency on the quality of output V & I](image)

<table>
<thead>
<tr>
<th>Inverter Frequency</th>
<th>THD of Voltage</th>
<th>THD of Current</th>
</tr>
</thead>
<tbody>
<tr>
<td>100 Hz</td>
<td>84.35%</td>
<td>176.25%</td>
</tr>
<tr>
<td>1 kHz</td>
<td>5.240%</td>
<td>35.28%</td>
</tr>
<tr>
<td>10 kHz</td>
<td>4.280%</td>
<td>5.450%</td>
</tr>
<tr>
<td>15 kHz</td>
<td>4.210%</td>
<td>4.650%</td>
</tr>
</tbody>
</table>

C) Input and Output Power

Figure 10 shows the input and output power (with and without the proposed controller) under real irradiances (see Fig. 2). It is seen that the output power is close to input power of a PV array and its value is increased from 100 W to 540 W due to
using the proposed control. Also, note that the power factor of a load is 0.96 lagging.

Fig. 8. FFT analysis of voltage waveform

Fig. 9. FFT analysis of current waveform

Fig. 10. Daily power waveform under real solar irradiances

5. Conclusion

This paper demonstrates the performance of a grid-connected PV power system implemented using MATLAB/Simulink under real solar irradiances of a case study. A PV array is connected with a boost inverter in order to control the input current and output voltage (as well as frequency). In this paper, the proposed voltage-current mode controller adjusts the duty cycles of boost converter under varying irradiances to keep the peak value of input current less than the short-circuit current of a PV array and to obtain a constant sinusoidal output voltage at a constant frequency. It is found that in case of using the proposed controller, the peak input current is approximate 27% of $I_{sc}$ compared with 155% of $I_{sc}$ in case of using a classical controller. Also, in case of using the proposed controller, the ripple current is reduced by 50%. Finally, with using the proposed controller, the output ac power is 5-6 times higher than without using a controller.

References